

NOTIFICATION OF TRANSMITTAL OF COPIES OF TRANSLATION OF THE INTERNATIONAL PRELIMINARY **EXAMINATION REPORT**

(PCT Rule 72.2)

From the INTERNA	LBUREAU
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13. Dez. 2004 Date of mailing (day/month/year) **FRIST** 09 December 2004 (09.12.2004) TERM Applicant's or agent's file reference IMPORTANT NOTIFICATION 03SGL0136WOP

International application No. PCT/EP2003/003882 International filing date (day/month/year) 15 April 2003 (15.04.2003)

ALLEMAGNE

Applicant

SCHOTT AG et al

1. Transmittal of the translation to the applicant.

The International Bureau transmits herewith a copy of the English translation made by the International Bureau of the international preliminary examination report established by the International Preliminary Examining Authority.

Transmittal of the copy of the translation to the elected Offices.

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3. Reminder regarding translation into (one of) the official language(s) of the elected Office(s).

The applicant is reminded that, where a translation of the international application must be furnished to an elected Office, that translation must contain a translation of any annexes to the international preliminary examination report.

It is the applicant's responsibility to prepare and furnish such translation directly to each elected Office concerned (Rule 74.1). See Volume II of the PCT Applicant's Guide for further details.

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Translation

PATENT COOPERATION TREAT



PCT

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference 03SGL0136WOP	FOR FURTHER ACTION		cation of Transmittal of International Examination Report (Form PCT/IPEA/416)	
International application No	International filing date (day/i	nonth/year) -	Priority date (day/month/year)	
PCT/EP2003/003882	15 April 2003 (15.04	1.2003)	15 April 2002 (15.04.2002)	
International Patent Classification (IPC) or n C23C 14/10	ational classification and IPC			
Applicant	SCHOTT AG			
This international preliminary exammed and is transmitted to the applicant according to the ac		l by this Intern	ational Preliminary Examining Authority	
2. This REPORT consists of a total of	5 sheets, includi	ng this cover s	heet.	
amended and are the basis for	ied by ANNEXES, i.e., sheets or this report and/or sheets conta Administrative Instructions und	ining rectifica	on, claims and/or drawings which have been tions made before this Authority (see Rule	
These annexes consist of a to	otal of sheets.	<i>:</i> . ·		
3. This report contains indications rela	ting to the following items:			
I Basis of the report	•			
II Priority		٠.		
III Non-establishment	of opinion with regard to novelt	y, inventive st	ep and industrial applicability	
IV Lack of unity of inv	ention			
V Reasoned statement citations and explan	under Article 35(2) with regard ations supporting such statement	l to novelty, in it	ventive step or industrial applicability;	
VI Certain documents of	cited			
VII Certain defects in the	ne international application			
VIII Certain observations on the international application				
Date of submission of the demand	Date of	f completion of	of this report	
29 August 2003 (29.08	.2003)	26 <i>A</i>	August 2004 (26.08.2004)	
Name and mailing address of the IPEA/EP	Autho	rized officer		
Facsimile No.	Telepi	ione No.	·	

INTERNATIONAL PRELID. ARY EXAMINATION REPORT

Ι.	Basis	of the re	port	·	· .	:				
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INTERNATIONAL PRE INARY EXAMINATION REPORT

International	application No.
PEP	03/03882

V. .	Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial	applicability;
•	citations and explanations supporting such statement	

Statement			
Novelty (N)	Claims	1-32	YES
	Claims		NO .
Inventive step (IS)	Claims	1-32	YES
. •	Claims		NO NO
Industrial applicability (IA)	Claims	1-32	YES
	Claims		NO

Citations and explanations

This report makes reference to the following documents:

D1: WOO-BEOM CHOI ET AL: "Anodic bonding technique under low temperature and low voltage using evaporated glass", Journal of Vacuum Science & Technology B (Microelectronics and Nanometer Structures), Vol. 15, No. 2, pages 477-481, March-April 1997

D2: CH 387 175 A (WESTERN ELECTRIC CO) 31 January 1965 (1965-01-31)

D3: US-A-4 374 391 (CAMLIBEL IRFAN ET AL) 15 February 1983 (1983-02-15)

Document D1 is considered to constitute the prior art closest to the subject matter of claim 1 and discloses (the references in parentheses are to that document): a method for forming the housing of electronic components (abstract; page 481, last sentence) in which an evaporated glass source is used to apply a glass layer on one side of the substrate by vaporisation (page 477, Chapter II). The subject matter of claim 1 therefore differs from the known method in that the opposite side of the substrate is then processed so as to generate line contacts. Similarly, the method as per claim 1 differs from the methods in documents D2 (see claims I; II, 1.1; page 7, line 67 to

page 8, line 46) and D3 (see column 4, lines 44-51). The subject matter of claim 1 is therefore novel (PCT Article 33(2)).

The present invention can therefore be considered to address the problem of producing connection structures to electronic components already protected by a layer of evaporated glass. The solution to this problem, as proposed in claim 1 of the present application, involves an inventive step (PCT Article 33(3)) because this method cannot be derived from the prior art.

Claims 2-22 are dependent on claim 1 and therefore likewise meet the PCT novelty and inventive step requirements.

Although documents D1, D2 and D3 describe electronic components entirely or partially coated with an evaporated glass layer, the subject matter of claim 23 is not known from or suggested by these citations. According to claim 23, the connection structures on the component are located, owing to its production process, on the side of the substrate away from the glass layer. Claim 23 therefore meets the PCT novelty and inventive step requirements.

Claims 24-32 are dependent on claim 23 and therefore likewise meet the PCT novelty and inventive step requirements.

Since the invention is used in the electronic industry, it is industrially applicable.